WHAT IS CLAIMED IS:

1. An electronic device connected to at least one fan, which produces a plurality of noises and at least one signal during its operation, the electronic device comprising:

a signal detection unit for detecting the at least one signal produced by the at least one fan; and

a control processing unit for making a judgement according to the at least one signal and controlling playing of music accordingly.

2. The electronic device according to claim 1, wherein the signal detection unit comprises a counter.

3. The electronic device according to claim 1, wherein the signal detection unit comprises a super I/O component.

4. The electronic device according to claim 1, wherein the signal detection unit comprises a clock counter.

5. The electronic device according to claim 1, wherein the signal is a fan rotating speed signal.

6. The electronic device according to claim 1, wherein the at least one signal is a pulse or a square wave signal of the at least one fan.

7. The electronic device according to claim 1, wherein the control processing unit is an application specific integrated circuit (ASIC).

8. The electronic device according to claim 1, wherein the control processing unit is a microprocessor.

9. The electronic device according to claim 1, wherein the control processing unit is a central processing unit.

10. The electronic device according to claim 1, wherein the music comprises at least one opus.

11. The electronic device according to claim 1, wherein the control processing unit stores at least one program.

12. The electronic device according to claim 11, wherein the at least one program defines at least one default interval, which has at least one limit value.

13. The electronic device according to claim 12, wherein the limit value is a maximum of the default interval.

14. The electronic device according to claim 12, wherein the limit value is a minimum of the default interval.

15. The electronic device according to claim 12, wherein the control processing unit compares the at least one signal with the at least one limit value to judge whether or not a value of the at least one signal falls within the at least one default interval.

16. The electronic device according to claim 15, wherein the at least one program defines the music corresponding to the at least one default interval.

17. The electronic device according to claim 15, wherein the at least one program defines a playing speed for the music corresponding to the at least one default interval.

18. The electronic device according to claim 15, wherein the at least one program defines a playing volume for the music corresponding to the at least one default interval.

19. The electronic device according to claim 1, wherein the control processing unit comprises a memory unit for storing the music.

20. The electronic device according to claim 1 further comprising a memory for storing the music.

21. The electronic device according to claim 1 further comprising a hard disk for storing the music.

22. The electronic device according to claim 1 being a motherboard.

23. The electronic device according to claim 1 being connected to a speaker, which is coupled to the control processing unit.

24. An electronic apparatus, comprising:

at least one fan, which produces a plurality of noises and at least one signal during its operation;

a signal detection unit, for detecting the at least one signal produced by the at least one fan; and

a control processing unit, for making a judgement according to the at least one signal and controlling playing of music accordingly.

25. The electronic apparatus according to claim 11, wherein the signal detection unit comprises a counter.

26. The electronic apparatus according to claim 24, wherein the signal detection unit comprises a super I/O component.

27. The electronic apparatus according to claim 24, wherein the signal detection unit comprises a clock counter.

28. The electronic apparatus according to claim 24, wherein the signal is a fan rotating speed signal.

29. The electronic apparatus according to claim 11, wherein the at least one signal is a pulse or a square wave signal of the at least one fan.

30. The electronic apparatus according to claim 24, wherein the control processing unit is an application specific integrated circuit (ASIC).

31. The electronic apparatus according to claim 24, wherein the control processing unit is a microprocessor.

32. The electronic apparatus according to claim 24, wherein the control processing unit is a central processing unit.

33. The electronic apparatus according to claim 24, wherein the music comprises at least one opus.

34. The electronic apparatus according to claim 24, wherein the control processing unit stores at least one program.

35. The electronic apparatus according to claim 34, wherein the at least one program defines at least one default interval, which has at least one limit value.

36. The electronic apparatus according to claim 35, wherein the limit value is a maximum of the default interval.

37. The electronic apparatus according to claim 35, wherein the limit value is a minimum of the default interval.

38. The electronic apparatus according to claim 35, wherein the control processing unit compares the at least one signal with the at least one limit value to judge whether or not a value of the at least one signal falls within the at least one default interval.

39. The electronic apparatus according to claim 38, wherein the at least one program defines the music corresponding to the at least one default interval.

40. The electronic apparatus according to claim 38, wherein the at least one

program defines a playing speed for the music corresponding to the at least one default interval.

41. The electronic apparatus according to claim 38, wherein the at least one program defines a playing volume for the music corresponding to the at least one default interval.

42. The electronic apparatus according to claim 24, wherein the control processing unit comprises a memory unit for storing the music.

43. The electronic apparatus according to claim 24 further comprising a memory for storing the music.

44. The electronic apparatus according to claim 24 further comprising a hard disk for storing the music.

45. The electronic apparatus according to claim 24 being a computer.

46. The electronic device according to claim 24 being connected to a speaker, which is coupled to the control processing unit.

47. A fan, which produces a plurality of noises and at least one signal during its operation, the fan comprising:

a circuit board;

a signal detection unit, disposed on the circuit board, for detecting the at least one signal produced by the at least one fan; and

a control processing unit, disposed on the circuit board, for making a judgement according to the at least one signal and controlling playing of music accordingly.

48. The fan according to claim 47, wherein the signal detection unit comprises a counter.

49. The fan according to claim 47, wherein the signal is a fan rotating speed signal.

50. The fan according to claim 47, wherein the at least one signal is a pulse or a square wave signal of the at least one fan.

51. The fan according to claim 47, wherein the control processing unit is an application specific integrated circuit (ASIC).

52. The fan according to claim 47, wherein the control processing unit is a microprocessor.

53. The fan according to claim 47, wherein the music comprises at least one opus.

54. The fan according to claim 47, wherein the control processing unit stores at least one program.

55. The fan according to claim 54, wherein the at least one program defines at least one default interval, which has at least one limit value.

56. The fan according to claim 55, wherein the limit value is a maximum of the default interval.

57. The fan according to claim 55, wherein the limit value is a minimum of the default interval.

58. The fan according to claim 55, wherein the control processing unit compares the at least one signal with the at least one limit value to judge whether or not a value of the at least one signal falls within the at least one default interval.

59. The fan according to claim 58, wherein the at least one program defines the music corresponding to the at least one default interval.

60. The fan according to claim 58, wherein the at least one program defines

a playing speed for the music corresponding to the at least one default interval.

61. The fan according to claim 58, wherein the at least one program defines a playing volume for the music corresponding to the at least one default interval.

62. The fan according to claim 47, wherein the control processing unit comprises a memory unit for storing the music.

63. The fan according to claim 47 further comprising a memory for storing the music.

64. The electronic device according to claim 47 being connected to a speaker, which is coupled to the control processing unit.

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